DS04-11108-3E

ASSP QUAD OPERATIONAL AMPLIFIER

MB3615

QUAD OPERATIONAL AMPLIFIER OPERATES FROM A SINGLE OR DUAL POWER SUPPLY

The Fujitsu MB3615 is a Quad operational amplifier having a phase compensatory circuit and operates from a single power supply or dual power supplies.

The device has equivalent electrical characteristics of current industrial standard operational amplifier and requires low power supply current.

MB3615 can be mounted in high density because it integrates 4 circuits on a chip in 14pin package. It is taking the countermeasure for cross-over distortion, so can be used for amplifying AC.

The MB3615 is pin compatible with Motorola MC3303.

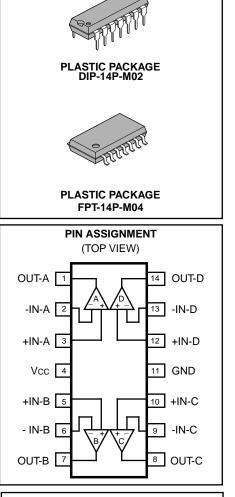
- · No phase compensation required
- Wide power supply voltage
 - Single power supply: +3 to +30 V
 - Dual power supplies: ±1.5 to ±15 V
- Wide input common mode range: 0 to (Vcc -1.5) V
- Low power supply current: 2 mA typ.
- Low Cross-over distortion

ABSOLUTE MAXIMUM RATINGS (see NOTE)

			Ta=25°C
Rating	Symbol	Value	Unit
Power Supply Voltage *	Vcc	36	V
Differential Input Voltage *	Vid	36	V
Input Common Mode Voltage *	Vı	-0.3 to +36	V
Power Dissipation	PD	570	mW
Operating Temperature	Тор	-20 to +75	°C
Storage Temperature	Тѕтс	-55 to +125	°C

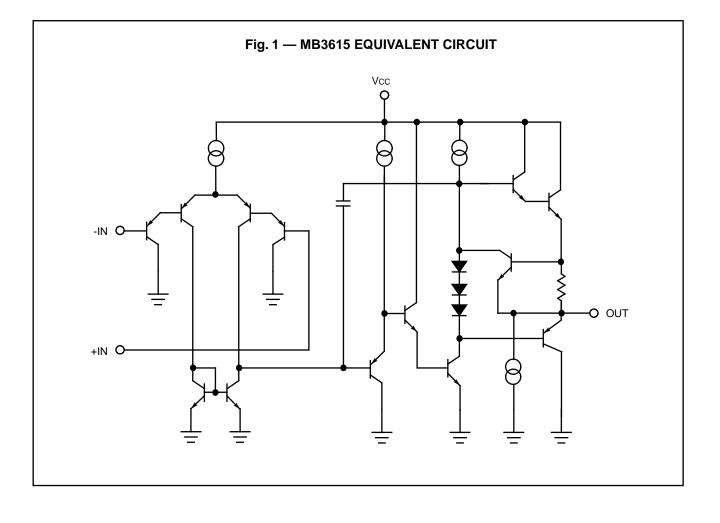
NOTE: * Single Power Supply.

Permanent device damage may occur if the above **Absolute Maximum Ratings** are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.

MB3615



ELECTRICAL CHARACTERISTICS

			$(VCC = +15V, VEE = -15V, Ta = 25^{\circ}C)$			
Parameter	Symbol	Condition	Value			l Init
			Min	Тур	Max	Unit
Input Offset Voltage	Vio	-	-	2	7	mV
Input Offset Current	lio	-	-	5	50	nA
Input Bias Current	lı*	-	-	45	250	nA
Power Supply Current	Icc	RL = ∞	-	2.0	4.0	mA
Input Common Mode Voltage	Vсм	-	VEE	-	Vcc-1.5	V
Voltage Gain	Av	$RL \ge 2k\Omega$	20	100	-	V/mV
Output Voltage Range	Vом -	$RL \ge 2k\Omega$	±10	±12	-	V
		$RL = 2k\Omega$	±12	±13	-	V
Output Current	ISOUECE	-	10	40	-	mA
	Isink	-	10	20	-	mA
Common Mode Rejection Ratio	CMRR	-	70	85	-	dB
Power Supply Voltage Rejection Ratio	SVRR	-	65	100	-	dB
Channel Separation	CS	-	-	120	-	dB

NOTE:

* A direction of the input bias current flows from IC because first input transistor consists of PNP.

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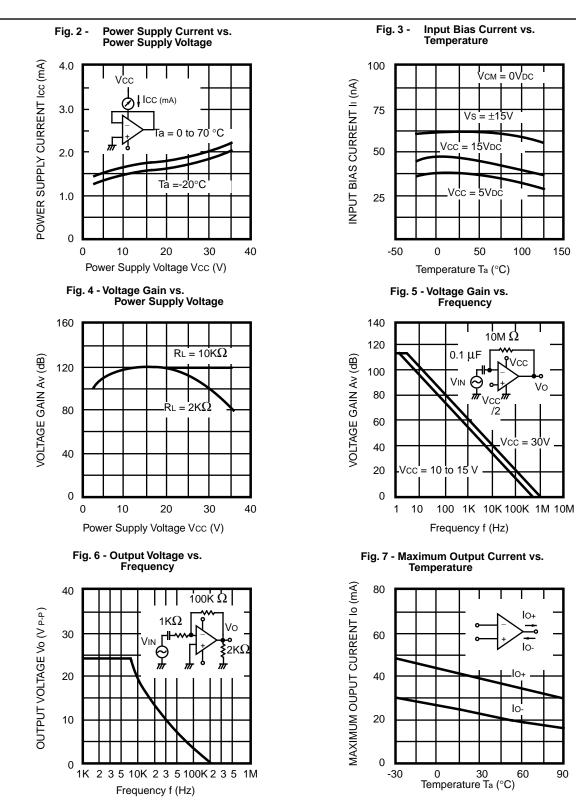
60

90

150

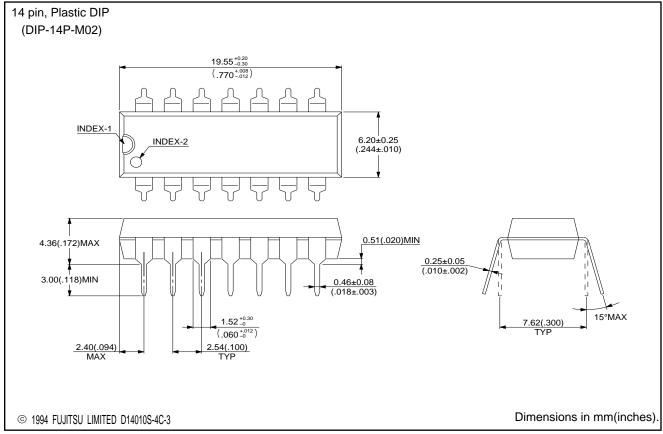
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TYPICAL CHARACTERISTICS CURVES



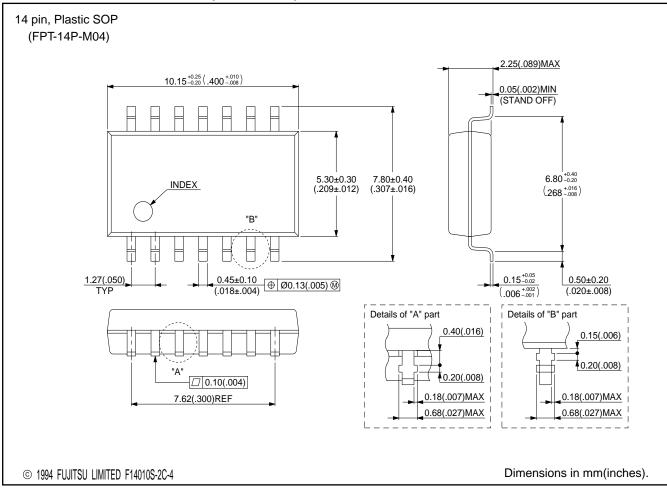
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■ PACKAGE DIMENSIONS



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PACKAGE DIMENSIONS (Continued)



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